



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

5 In re Application f: Chong et al. D cket No.: NNEX0002
Serial No.: 09/979,551 Art Unit: 2829
10 Filed: 21 November 2001 Examiner: Hollington, Jermele M.
Title: Massively Parallel Interface for Electronic Circuits

10 December 2003

15 Assistant Commissioner for Patents
Mail Stop Non Fee Amendment
P.O. Box 1450
Washington, DC 20231

RECEIVED
DEC 24 2003
TECHNICAL CENTER 2800

20 **AMENDMENT IN RESPONSE TO OFFICE ACTION**

Sir:

This is in response to the Office Action filed by the Examiner, which was mailed to
25 Applicant on 10 September 2003, for the above-identified patent application. Applicant
considers this document to be filed in a timely manner.

The Application, as originally filed, comprised 78 total claims, with 4 independent claims.
Currently, there are 47 total claims withdrawn, having 2 independent claims withdrawn. This
30 Amendment comprises 30 new claims, having 2 new independent claims, such that the
Application currently comprises 61 total claims, with 4 independent claims. Applicant
therefore submits that no fees are currently due for the newly entered claims.

A telephone interview was conducted between the Examiner, Jermele M. Hollington,
35 Applicant's Representative and Patent Agent, Donald M. Hendricks, and Douglas N.
Modlin, a consultant for the Applicant, on Wednesday, 10 December 2003, to discuss the
present invention, the cited art of Higgins et al and Smith et al, the present objections to the
drawings under 37 CFR 1.83(a), and the present rejections under 35 U.S.C. §112, 35
U.S.C. §102(b), and 35 U.S.C. §103(a).

40

02/04/2004 J MURPHY 00000001 071445 09979551

01 FC:1201 172.00 DA
02 FC:1202 540.00 DA